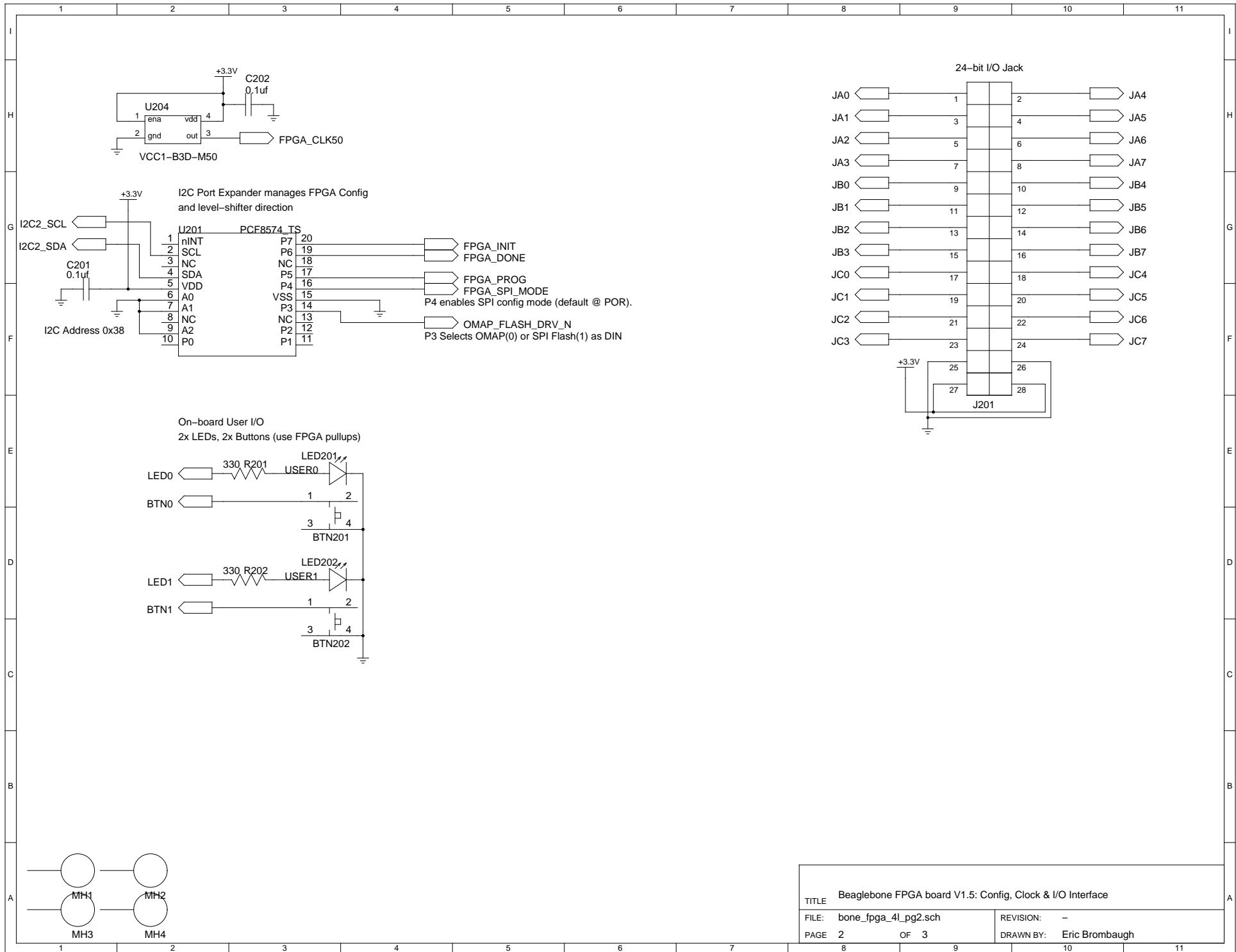


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| TITLE Beaglebone FPGA board V1.5: Beaglebone Interface | |
| FILE: bone_fpga_4l_pg1.sch | REVISION: - |
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|---|-------------|--------------------------|
| TITLE Beaglebone FPGA board V1.5: Config, Clock & I/O Interface | | |
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