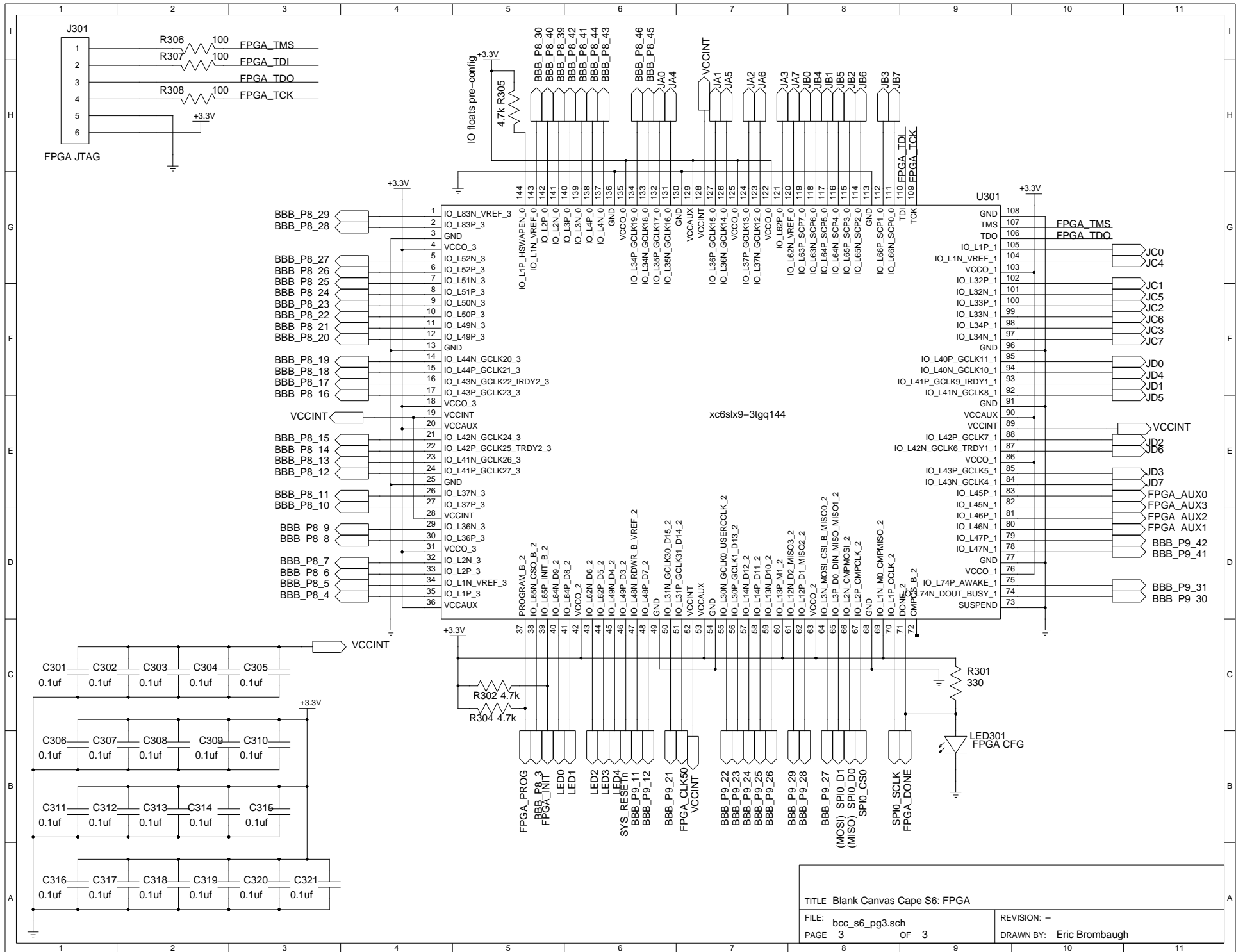


TITLE Blank Canvas Cape S6 V0.1: Config, Clock & I/O Interface		
FILE: bcc_s6_pg2.sch	REVISION: -	
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TITLE Blank Canvas Cape S6: FPGA		REVISION: -
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